

Quad QSFP28/QSFP+ FMC+ Module



iWave's 2x1 stacked QSFP28/QSFP+ FMC+ HPSC Module is designed to meet VITA 57.4 FMC+ Standard.

This FPGA Mezzanine Connector Plus (FMC+) Module supported by two Stacked QSFP28/QSFP+ ports (upto 400G) with 4-Bit Silicon Labs programmable clock synthesizer (default = 156.25 MHz). A flexible reference clock for on the Quad QSFP28/QSFP+ Module is fully programmable over the 0.16 to 710 MHz range through I2C. Each QSFP28/QSFP+ ports is directly connected to four multi-gigabit serial transceivers banks of Vita 57.4 complaint carrier board.

HIGHLIGHTS

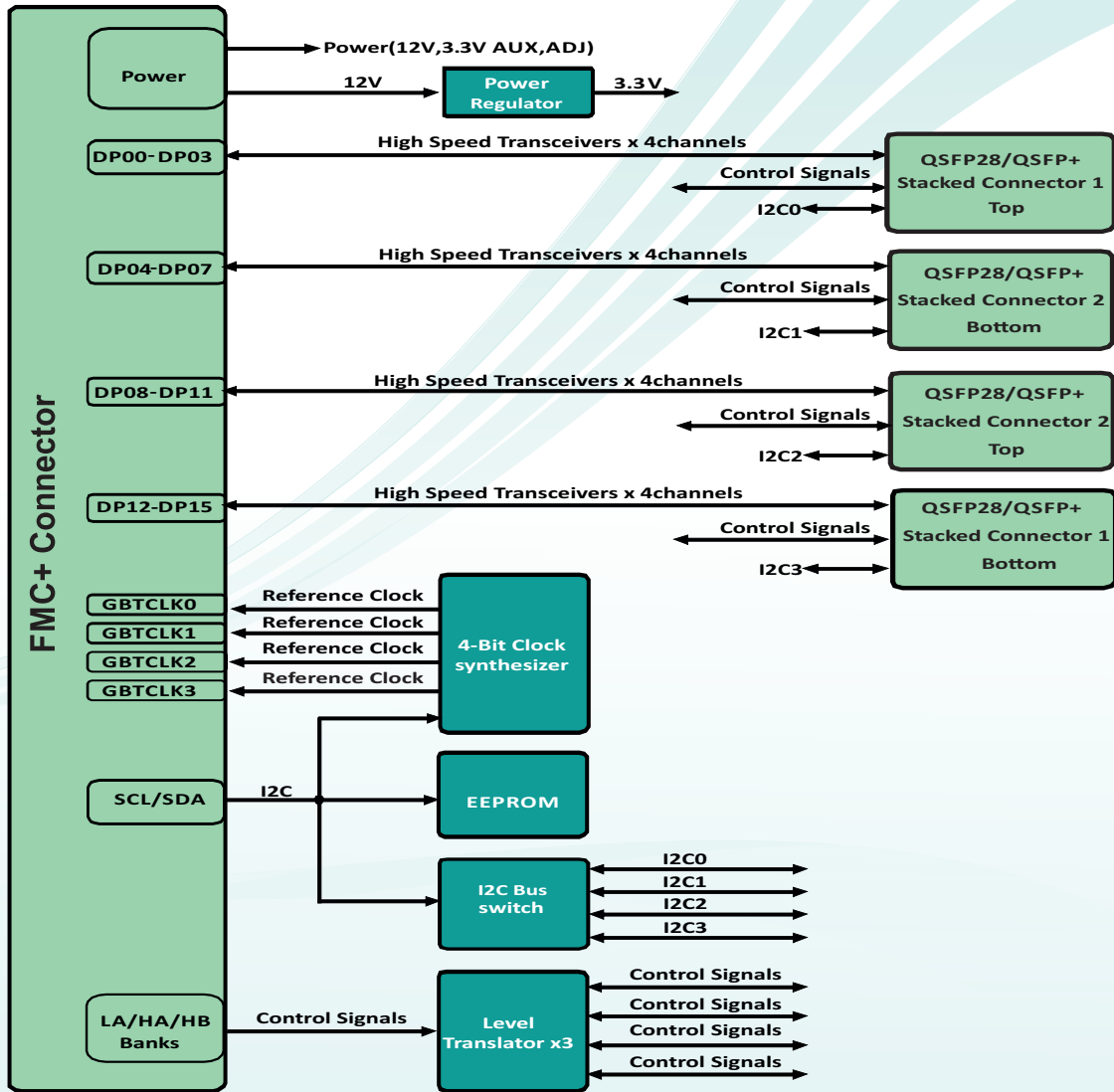
- VITA 57.4 FMC+ HSPC Connector
- Two 2x1 Stacked QSFP28/QSFP+ Connectors
- 4bit Programmable clock synthesizer
- 32Kb-EEPROM

SPECIFICATIONS

On Module Features	Power
FMC+ Connector	12V from FMC+ HSPC Connector
VITA 57.4 FMC+ HSPC Connector	Operating Temperature
QSFP+ Connector	-40°C to +85°C (Industrial Grade)
QSFP28/QSFP+ Connector supported upto 400G	Form Factor
Clock Synthesizer	VITA 57.4 Single Width (84.81mm x 69mm)
4-bit programmable clock synthesizer	Compliance
EEPROM	RoHS & REACH Compliant
32Kb EEPROM for VITA 57.4 Configuration storage	CE*

* Under Progress

Quad QSFP28/QSFP+ FMC+ Module Block Diagram



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**Optional items not included in the standard deliverables.*

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The device can be ordered online from the **iWave Website**
<https://www.iwavesystems.com/product/quad-qsfp28/qsfp+-fmc+-module/>
 Or from our **Local Partners** in your region
<http://www.iwavesystems.com/about-us/business-partner.html>

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